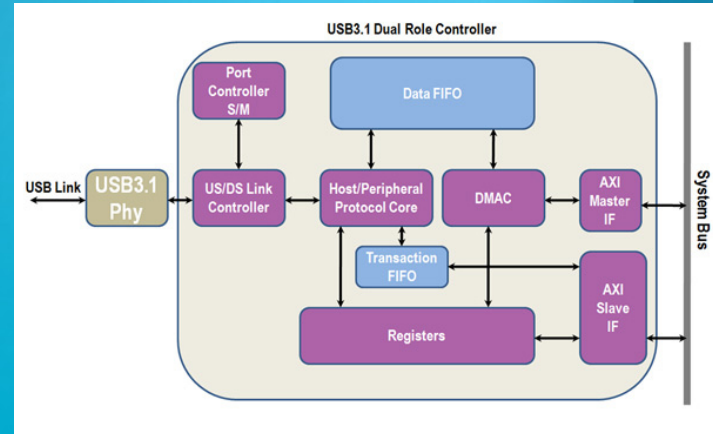


Overview

Innovative Logic USB 3.1 Device and Host dual role controller IP is compliant with USB3.1 specification. USB 3.1 device and host dual role IP is able to function both as an Embedded USB3.1 Host and a USB3.1 peripheral while operating at SuperSpeedPlus speed of 10 Gbps. An external selection input determines the role the controller as device or host. As Host Innovative Logic USB 3.1 dual role controller IP is capable of handling multiple devices.

Innovative Logic USB 3.1 dual role controller IP support multiple System bus Interfaces – AHB/AXI/PLB with 32/64 bit data bus and is fully backward compatible with USB 3.0 IP.



Features

- Compliant with:
 - USB 3.1 IP specification
 - USB3.1 PIPE interface
 - Support 8/16/32 data bus width
- AXI, AHB, PLB Bus standards
 - Support 32/64 bit data bus
- Supports RSP – Role Swap Protocol
- Hub Class support
- Supports Super Speed OTG communication
- Support USB3.1 power down modes
- Support Control, Bulk, Isochronous and Interrupt transaction
- Configurable up to 15 transmit and 15 receipt endpoints apart from default endpoint
- Dynamically configurable Endpoint FIFO for optimum usage of memory
- Synchronous SRAM interface for FIFO
- Integrated DMA controller

Target Applications

- Removable hard disks
- High End Digital camera
- Printer, scanner etc
- Multimedia Applications
- Mobile phones and Tablets
- 4K/8K TV, DVD players, Set top Boxes

Deliverables

- Synthesizable RTL developed in Verilog
- Constraints for synthesis
- Test bench and Test cases developed using System Verilog
- Documentation

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