



**Leading Provider of SoC, ASIC,
FPGA, Embedded Software
Design Services**

Our Services

- ASIC Design & Verification
- UVM Based Verification
- FPGA Emulation
- DFT, BIST, Scan
- RTL to GDSII
- Embedded Software

Our Business Model

- Offshore Design Center (ODC)
- Turn-key with Fixed Cost
- On-Site Staff Augmentation

Our Customers



Our Partners



Corporate Headquarters

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ASIC, SoC Design/Verification Services

- Architecture, RTL, Synthesis, STA, LEC
- Verification: UVM, Systemverilog, C/C++
- DFT/DFX – Scan, ATPG, BIST
- RTL to GDSII – Cadence/Synopsys
- Mask Layout & Analog Mixed Signal

FPGA Validation/Emulation Services

- RTL, synthesis, integration
- Timing Closure and Timing Analysis
- Bring-up using Xilinx & Altera
- Prototyping and FPGA based Emulation
- ASIC to FPGA or FPGA to ASIC

Embedded Software Services

- Device Drivers, Protocol Stack, Assembly
- RTOS: Linux, Android, iOS, ThreadX, Windows
- Board bring up, boot loader, diagnostic
- DSP (firmware, algorithm, validation, verification)
- Embedded CPU – ARM, MIPS, PowerPC, x86, etc.
- AI, ML, IOT, Cloud, networking, Security, Storage, Video, Graphics